



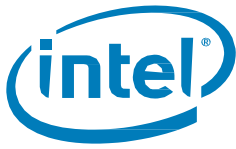
Intel® Solid-State Drive Pro 1500 Series (2.5-inch)

Product Specification

- Capacities: 80 GB, 120 GB, 180 GB, 240 GB, 360 GB, 480 GB
- Components:
 - Intel® 20nm NAND Flash Memory
 - Multi-Level Cell (MLC)
- Form Factors: 2.5 inch
- Thickness: 7 mm
- Weight: Up to 78 grams
- SATA 6Gb/s Bandwidth Performance¹ (Iometer* Queue Depth 32)
 - Sustained Sequential Read: up to 540 MB/s
 - Sustained Sequential Write: up to 490 MB/s
- Read and Write IOPS¹ (Iometer Queue Depth 32)
 - Random 4 KB Reads: up to 41,000 IOPS
 - Random 4 KB Writes: up to 80,000 IOPS²
- Latency (average sequential)
 - Read: 80 µs (TYP)
 - Write: 85 µs (TYP)
- Data Compression
- AES 256-bit Encryption
- End-to-End Data Protection
- Compatibility
 - TCG* Opal Security Subsystem Class (SSC) Specification Version 1.0³
 - Intel® Core™ vPro™ Processor Enrichment
 - Intel SCS 9.0
 - Intel® SSD Toolbox with Intel® SSD Optimizer
 - Intel® Data Migration Software
 - Intel® Rapid Storage Technology
 - SATA Revision 3.0
 - ACS-2 (ATA/ATAPI Command Set 2)
 - SSD Enhanced SMART ATA feature set
- Power Management
 - 5.0 V SATA Supply Rail
 - SATA Link Power Management (LPM)
 - Device Sleep (DevSleep)
- Power
 - Active (BAPCo MobileMark* 2007 Workload): 195 mW
 - Idle⁴: 125 mW
 - DevSleep: 5 mW
- Temperature
 - Operating⁵: 0° C to 70° C
 - Non-Operating: -55° C to 95° C
- Reliability
 - Uncorrectable Bit Error Rate (UBER): <1 sector per 10¹⁶ bits read
 - Mean Time Between Failure (MTBF): 1,200,000 hours
 - Shock (operating and non-operating): 1,500 G/0.5 msec
- Vibration
 - Operating: 2.17 G_{RMS} (5-700 Hz)
 - Non-operating: 3.13 G_{RMS} (5-800 Hz)
- Certifications and Declarations:
 - UL*
 - CE*
 - C-Tick*
 - BSMI*
 - KCC*
 - Microsoft* WHCK
 - VCCI*
 - SATA-IO*
- Product Ecological Compliance
 - RoHS*

NOTES:

1. Performance values vary by capacity.
2. Random 4 KB writes measured using out-of-box SSD.
3. Opal available only on specific firmware SKUs. Contact your Intel representative for more info. ID Device Word 48 bit 0 = 1 if Opal Ready.
4. Non-DevSleep idle power with SATA Link Power Management (LPM) enabled.
5. As measured by temperature sensor, SMART Attribute BEh. Active airflow is recommended within the system for maintaining proper device operating temperatures on heavier workloads.



Ordering Information

Contact your local Intel sales representative for ordering information.

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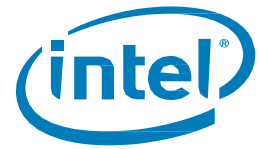
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Revision History

Document Number	Revision Number	Description	Revision Date
329389	001US	Initial release	August 2013
329389	002US	Corrected 4K read specification	September 2013
329389	003US	Added the 80 GB, 360 GB and 480 GB capacities	October 2013



1 Introduction

This document describes the specifications and capabilities of the Intel® Solid-State Drive Pro 1500 Series (Intel® SSD Pro 1500 Series).

The Intel SSD Pro 1500 Series delivers the next generation storage solution with new low power features to align with 2013 Ultrabook™ platform requirements. The next generation Intel SSD continues to provide high performance expected for Serial Advanced Technology Attachment (SATA)-based computers.

The industry-standard 2.5-inch form factor enables interchangeability with existing hard disk drives (HDDs) and native SATA HDD drop-in replacement with the enhanced performance, reliability, and ruggedness offered by an SSD.

The latest feature addition is the DevSleep functionality, a low power drive state controlled by the host via the DevSleep pin. The drive will consume a mere 5 mW while in this state.

As compared to standard SATA HDDs, Intel SSD Pro 1500 Series offers these key features:

- High I/O and throughput performance
- Low power consumption
- Increased system responsiveness
- High reliability
- Enhanced ruggedness

The Intel SSD Pro 1500 Series also offers additional key features such as:

- Advanced Encryption Standard (AES) 256-bit Encryption

AES 256-bit encryption is an industry standard in data security, providing a hardware-based mechanism for encryption and decryption of user data. Utilizing a 256-bit encryption key, AES encryption—when combined with an ATA drive password—helps protect user data.

- End-to-End Data Protection

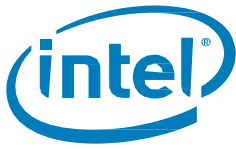
End-to-end data protection helps protect data from being corrupted across the data path by using cyclic redundancy check (CRC), parity, and error correction code (ECC) checks in the data path from the host interface to the NAND, and back.

- Data Compression

Data compression helps improve performance and endurance by automatically compressing information sent to the SSD so that less data has to be processed and stored on the NAND. The amount of data that can be compressed depends on the type of data.

- TCG* Opal Security Subsystem Class (SSC) Specification Version 1.0 (Select firmware only)

Intel® SSD Pro 1500 Series self-encrypting drive capabilities are enhanced with Opal security storage specifications. The TCG* Opal specification provides a secure boot capability (pre-boot authentication), protection of user data, and administrative capabilities that help protect user data from compromise, improving security of critical data on the move.



- Intel® Core™ vPro™ Processor Enrichment

Intel® SSD Pro 1500 Series is enhanced when coupled with Intel® Core™ vPro™ processors systems and software. Offering unique manageability capabilities, IT can proactively manage the Intel® SSD Pro1500 Series products and reduce operating costs while keeping their PCs running smoothly.

- Intel® SIPP

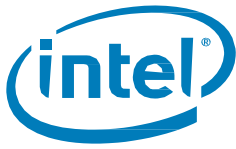
Intel® SSD Pro series will be part of the Intel Stable Image Platform Program (SIPP). Intel® SIPP aligns key Intel platform components, enabling a predictable transition from one technology generation to the next. Enhancing software and hardware stability, Intel® SIPP targets zero changes to key platform components and drivers for at least 15 months. Intel® SIPP delivers predictability and reduces total cost of ownership.



1.1 Terminology

Table 1: Terminology

Term	Description
ATA	Advanced Technology Attachment
DAS	Device Activity Signal
DevSleep	Device Sleep, a new addition to the SATA specification
DIPM	Device Initiated Power Management
DMA	Direct Memory Access
EXT	Extended
FPDMA	First Party Direct Memory Access
GB	Gigabyte (1,000,000,000 bytes) Note: The total usable capacity of the SSD may be less than the total physical capacity because a small portion of the capacity is used for NAND flash management and maintenance purposes.
HDD	Hard Disk Drive
HIPM	Host Initiated Power Management
I/O	Input/Output
IOPS	Input/Output Operations Per Second
KB	Kilobyte (1,024 bytes)
LBA	Logical Block Address
LPM	Link Power Management
MB	Megabyte (1,000,000 bytes)
MLC	Multi-level Cell
MTBF	Mean Time Between Failures
NCQ	Native Command Queuing
NOP	No Operation
Opal	A Trusted Computing Group standard that defines an interface for managing a Self-Encrypting Drive (SED).
PIO	Programmed Input/Output
RDT	Reliability Demonstration Test
RMS	Root Mean Squared
SATA	Serial Advanced Technology Attachment
SMART	Self-Monitoring, Analysis and Reporting Technology
SSD	Solid-State Drive
TYP	Typical
UBER	Uncorrectable Bit Error Rate



1.2 Reference Documents

Table 2: Standard References

Date or Rev. #	Title	Location
May 2005	SFF-8201, 2.5-inch drive form factor	http://www.sffcommittee.org/
May 2006	SFF-8223, 2.5-inch Drive w/Serial Attachment Connector	http://www.sffcommittee.org/
Sept 2008	IEC 55022 Information Technology Equipment — Radio disturbance Characteristics— Limits and methods of measurement CISPR22:2008 (Modified)	http://www.iec.ch/
Dec 2008	VCCI	http://www.vcci.jp/vcci_e/
June 2009	RoHS	http://qdms.intel.com/ Click <i>Search MDDS Database</i> and search for material description datasheet
June 2009	Serial ATA Revision 3.0	http://www.sata-io.org/
August 2009	ACS-2 Specification	http://www.t13.org/
August 2010	IEC 55024 Information Technology Equipment — Immunity characteristics— Limits and methods of measurement CISPR24:2010	http://www.iec.ch/
Sept 2010	Solid-State Drive (SSD) Requirements and Endurance Test Method (JESD218)	http://www.jedec.org/standards-documents/docs/jesd218/



2 Product Specifications

2.1 Capacity

Table 3: User Addressable Sectors

Capacity	Unformatted Capacity (Total User Addressable Sectors in LBA mode)
80 GB	156,301,488
120 GB	234,441,648
180 GB	351,651,888
240 GB	468,862,128
360 GB	703,282,608
480 GB	937,703,088

2.2 Performance

Table 4: Compressible Performance

Capacity	Specification				
	Random 4 KB Read (up to) ¹	Random 4 KB Write (up to) ²	Random 4 KB Write (TYP) ¹	Sequential 128 KB Read ¹	Sequential 128 KB Write ¹
	IOPS	IOPS	IOPS	MB/s	MB/s
80 GB	24,000	80,000	33,000	540	480
120 GB	24,000	80,000	37,500	540	480
180 GB	41,000	80,000	49,000	540	490
240 GB	41,000	80,000	49,000	540	490
360 GB	45,000	80,000	33,000	540	490
480 GB	48,000	80,000	33,000	540	490

Note:

- Performance measured using Iometer* with Queue Depth 32. Measurements are performed on 8 GB of Logical Block Address (LBA) range on a full SSD.
- Random 4 KB writes measured using out-of-box SSD.

Table 5: Incompressible Performance

Capacity	Specification			
	Random 4 KB Read (up to) ¹	Random 4 KB Write (up to) ¹	Sequential 128 KB Read ¹	Sequential 128 KB Write ¹
	IOPS	IOPS	MB/s	MB/s
80 GB	20,500	13,000	430	100
120 GB	18,000	15,000	450	130
180 GB	37,500	17,000	470	170
240 GB	37,500	23,000	510	230
360 GB	37,500	10,000	525	240
480 GB	37,500	13,000	540	300

Note:

- Performance measured using Iometer* with Queue Depth 32

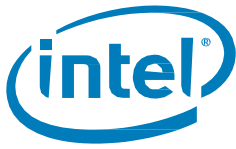


Table 6: Latency

Specification	Intel® SSD Pro 1500 Series					
	80 GB	120 GB	180 GB	240 GB	360 GB	480 GB
Read ¹	80 µs (TYP)					
Write ¹	85 µs (TYP)					
Power On To Ready ²	1 s (TYP)					

Note:

1. Based on sequential 4 KB using Iometer with Queue Depth 1 workload with compressible (non-random) data pattern. Write Cache enabled.
2. Power On To Ready time assumes proper shutdown

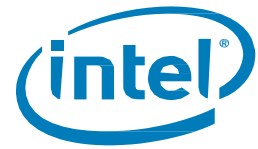
2.3 Electrical Characteristics

Table 7: Operating Voltage and Power Consumption

Electrical Characteristics	Value					
	80 GB	120 GB	180 GB	240 GB	360 GB	480 GB
Operating Voltage for 5 V (±5%)						
Min	4.75 V					
Max	5.25 V					
Rise Time (Max/Min)	100 ms / 0.1 ms					
Fall Time (Max/Min)	5 s / 1 ms					
Noise Tolerance	100 mV pp (10 Hz – 30 MHz)					
Min Off Time ¹	1 s					
Power Consumption (TYP)						
Active ²	195 mW					
Idle ³	125 mW					
DevSleep ⁴	5 mW					
Thermal Power ⁵	2.8 W	3.1 W	3.7 W	4.5 W	4.5 W	4.5 W
Regulator Power ⁶	3.4 W	3.4 W	4.0 W	4.9 W	5.0 W	5.0 W

Note:

1. Minimum time from when power removed from drive ($V_{cc} < 100$ mV) to when power can be reapplied to drive.
2. Active power measured during execution of MobileMark* 2007 with SATA Link Power Management (LPM) enabled.
3. Non-DevSleep idle power with SATA Link Power Management (LPM) enabled.
4. Power consumption during DevSleep state.
5. Power measured during 128 kB sequential writes with Queue Depth 32 workload using 100 ms sample period. This represents power that would be thermal load on system during heavy workloads.
6. Power measured during 128 kB sequential writes with Queue Depth 32 workload using 500 us sample period. This represents power that system power supply would have to regulate for proper device operation.



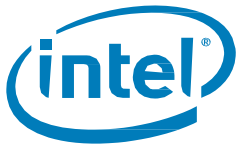
2.4 Environmental Conditions

Table 8: Temperature, Shock, Vibration

Electrical Characteristics	Range
Case Temperature	
Operating	0° C - 70° C
Non-operating ¹	-55° C - 95° C
Temperature Gradient ²	
Operating	30 (TYP) °C/hr
Non-operating	30 (TYP) °C/hr
Humidity	
Operating	5 – 95 %
Non-operating	5 – 95 %
Shock and Vibration	Range
Shock ³	
Operating	1,500 G (Max) at 0.5 msec
Non-operating	1,500 G (Max) at 0.5 msec
Vibration ⁴	
Operating	2.17 GRMS (5-700 Hz) Max
Non-operating	3.13 RMS (5-800 Hz) Max

Note:

1. Please contact your Intel representative for details on the non-operating temperature range.
2. Temperature gradient measured without condensation.
3. Shock specifications assume the SSD is mounted securely with the input vibration applied to the drive-mounting screws. Stimulus may be applied in the X, Y or Z axis. Shock specification is measured using peak acceleration and pulse width value.
4. Vibration specifications assume the SSD is mounted securely with the input vibration applied to the drive-mounting screws. Stimulus may be applied in the X, Y or Z axis. Vibration specification is measured using G Root Mean Squared (GRMS) value.

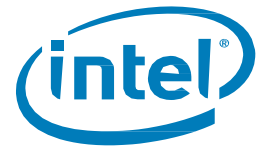


2.5 Product Regulatory Compliance

The Intel® SSD Pro 1500 Series meets or exceeds the regulatory or certification requirements in the table below.

Table 9: Product Regulatory Compliance Specifications

Title	Description	Region For Which Conformity Declared
TITLE 47-Telecommunications CHAPTER 1— FEDERAL COMMUNICATIONS COMMISSION PART 15 — RADIO FREQUENCY DEVICES	FCC Part 15B Class B	USA
ICES-003, Issue 4 Interference-Causing Equipment Standard Digital Apparatus	CAN/CSA – CEI/IEC CISPR 22-10 (Ref. CISPR 22:2008)	Canada
IEC 55024 Information Technology Equipment — Immunity characteristics— Limits and methods of measurement CISPR24:2010	EN-55024: 2010 and its amendments	European Union
IEC 55022 Information Technology Equipment — Radio disturbance Characteristics— Limits and methods of measurement CISPR22:2008 (Modified)	EN-55022: 2010 and its amendments	European Union
EN-60950-1 2 nd Edition	Information Technology Equipment — Safety — Part 1: General Requirements	USA/Canada
UL/CSA EN-60950-1 2 nd Edition	Information Technology Equipment — Safety — Part 1: General Requirements	USA/Canada



2.6 Reliability

The Intel® SSD Pro 1500 Series meets or exceeds SSD endurance and data retention requirements as specified in the JESD218 specification.

Table 10: Reliability Specifications

Parameter	Value
Uncorrectable Bit Error Rate (UBER) Uncorrectable bit error rate will not exceed one sector in the specified number of bits read. In the unlikely event of a non-recoverable read error, the SSD will report it as a read failure to the host; the sector in error is considered corrupt and is not returned to the host.	< 1 sector per 10^{16} bits read
Mean Time Between Failures (MTBF) Mean Time Between Failures is estimated based on Telcordia* methodology and demonstrated through Reliability Demonstration Test (RDT).	$\geq$ 1.2 million hours
Minimum Useful Life/Endurance Rating The SSD will have a minimum of five years of useful life under typical client workloads with up to 20 GB of host writes per day.	5 years
Insertion Cycles Maximum insertion/removal cycles on 2.5 inch port	250 insertion/removal cycles

2.7 Hot Plug Support

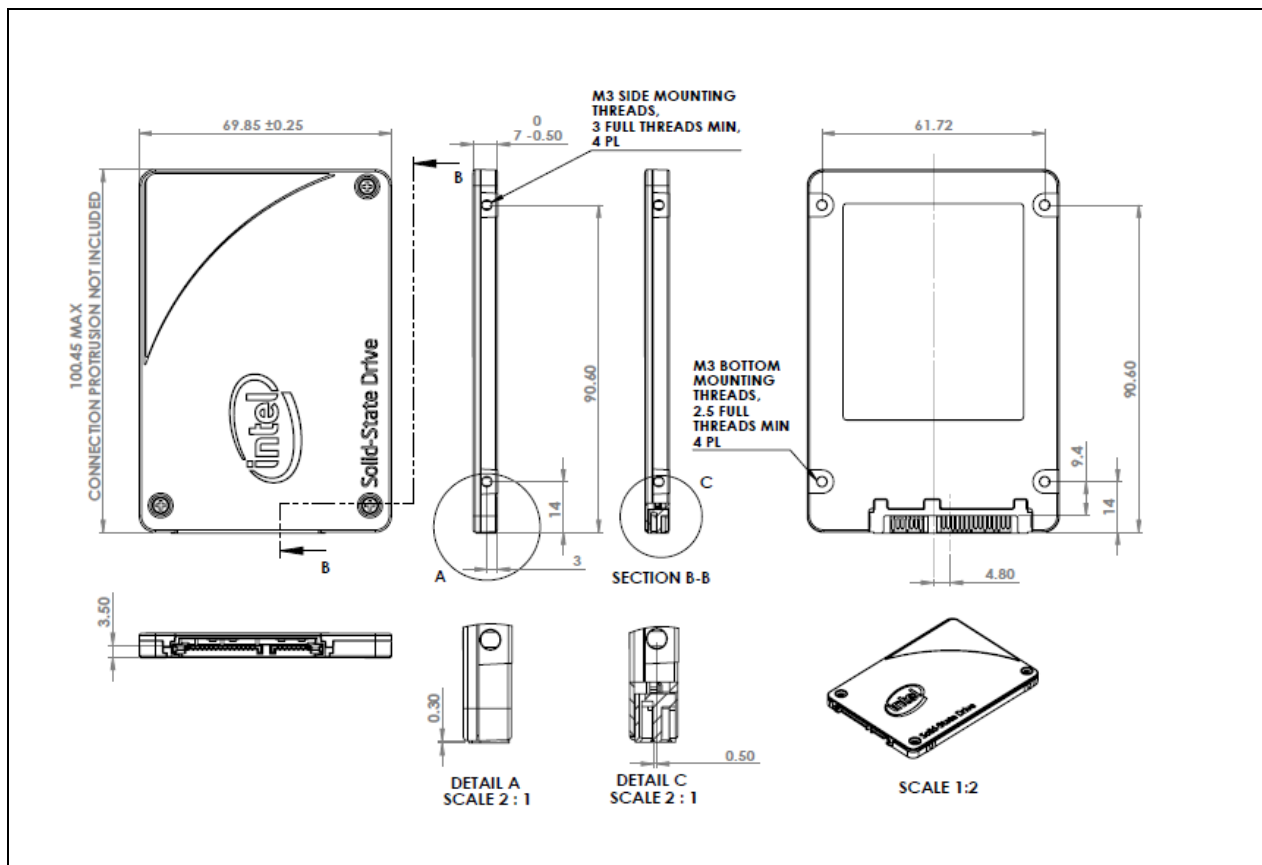
Hot Plug insertion and removal is supported in the presence of a proper connector and appropriate operation system, as described in the SATA 3.0 specification.

This product supports asynchronous signal recovery and issues an unsolicited COMINIT when first mated with a powered connector to guarantee reliable detection by a host system without hardware device detection.

3 Mechanical Information

The figure below shows the mechanical information for the full size 2.5 inch Intel® SSD Pro 1500 Series SSD. All dimensions are in millimeters.

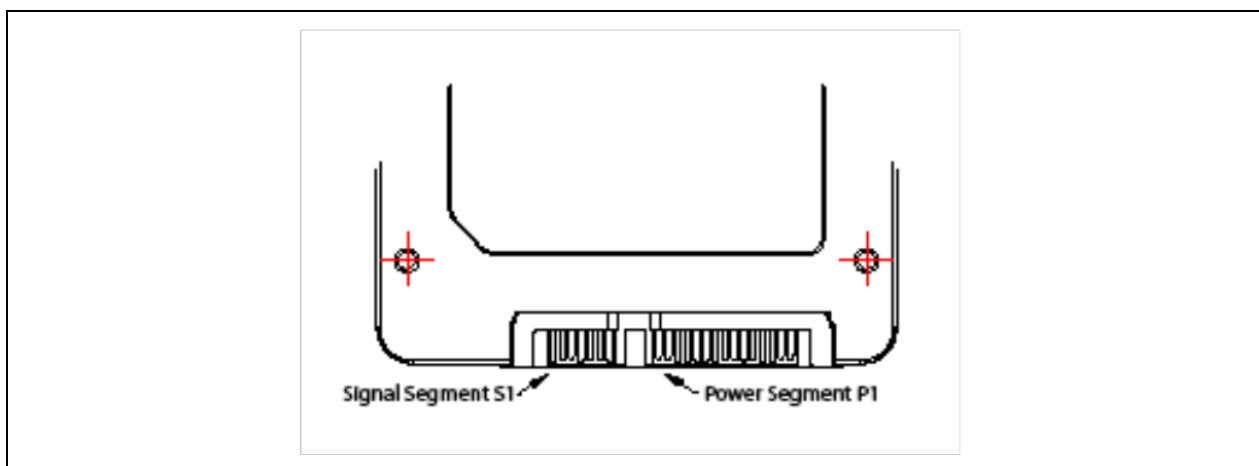
Figure 1: Dimensions for Full Size 2.5 inch Drives



4 Pin and Signal Descriptions

4.1 Pin Locations

Figure 2: Layout of Signal and Power Segment Pins



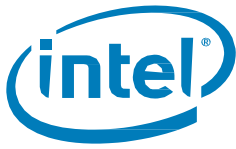
4.2 Signal Descriptions

4.2.1 Connector Pin Signal Definitions

Table 11: Serial ATA Connector Pin Signal Definitions

Pin	Function	Definition
S1	Ground	1st mate
S2	A+	Differential signal pair A
S3	A-	
S4	Ground	1st mate
S5	B-	Differential signal pair B
S6	B+	
S7	Ground	1st mate

Note: Key and spacing separate signal and power segments.



4.2.2 Power Pin Signal Definitions

Table 12: Serial ATA Power Pin Definitions

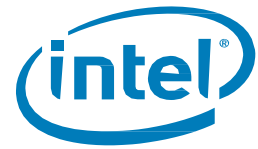
Pin ¹	Name	Definition	Mating Order
P1 ²	V ₃₃	3.3 V Power; not used	2nd Mate
P2 ²	V ₃₃	3.3 V Power; not used	2nd Mate
P3 ²	DevSleep	Device Sleep Pin	1st Mate
P4 ^{3,4}	Ground		1st Mate
P5 ³	Ground		1st Mate
P6 ³	Ground		1st Mate
P7 ^{3,5}	V ₅	5 V Power	1st Mate
P8 ^{3,5}	V ₅	5 V Power	2nd Mate
P9 ^{3,5}	V ₅	5 V Power	2nd Mate
P10 ³	Ground		1st Mate
P11 ⁶	DAS	Device Activity Signal	2nd Mate
P12 ^{3, 4}	Ground		1st Mate
P13 ²	V ₁₂	12 V Power; not used	1st Mate
P14 ²	V ₁₂	12 V Power; not used	2nd Mate
P15 ²	V ₁₂	12 V Power; not used	2nd Mate

Notes:

1. All pins are in a single row, with a 1.27 mm (0.050-inch) pitch.
2. Pins P1, P2 and P3 are connected together; Pins P13, P14 and P15 are connected together. Although they are not connected internally to the device, the host may apply voltage on these pins.
3. The mating sequence is:
 - Ground pins P4-P6, P10, P12 and the 5V power pin P7.
 - Signal pins and the rest of the 5V power pins P8-P9.
4. Ground connectors P4 and P12 may contact before the other 1st mate pins in both the power and signal connectors to discharge ESD in a suitably configured backplane connector.
5. Power pins P7, P8, and P9 are internally connected to one another within the device.
6. The host may ground P11 if it is not used for Device Activity Signal (DAS).

4.3 Device Sleep Feature

Device Sleep (or DevSleep/DEVSLP) is the latest feature aligned with Intel® 4th Generation Core™-based Ultrabook™. Ultrabook™ has stringent power requirements for SSDs and as such requires an ability to put the drive in a low power state. Although Link Power Management allows some control over power consumption, both methods still require the SATA link to remain online. The DevSleep pin is an enable high pin which is pulled up by the drive.



5 Supported Command Sets

The Intel SSD Pro 1500 Series supports all mandatory Advanced Technology Attachment (ATA) and Serial ATA (SATA) commands defined in the ACS-2 and SATA Revision 3.0 specifications. The mandatory and optional commands are defined in this section.

5.1 ATA General Feature Command Set

General Feature command set (non-PACKET), which consists of:

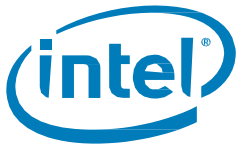
- EXECUTE DEVICE DIAGNOSTIC
- FLUSH CACHE
- IDENTIFY DEVICE ¹
- READ DMA
- READ SECTOR(S)
- READ VERIFY SECTOR(S)
- SEEK
- SET FEATURES
- WRITE DMA
- WRITE SECTOR(S)
- READ MULTIPLE
- SET MULTIPLE MODE
- WRITE MULTIPLE

The Intel SSD Pro 1500 Series also supports the following optional commands:

- READ BUFFER
- WRITE BUFFER
- NOP
- DOWNLOAD MICROCODE

Note:

1. See the Appendix for details on the sector data returned after issuing an IDENTIFY DEVICE command.



5.2 Power Management

The Intel SSD Pro 1500 Series supports several power management feature sets as defined by the ATA specification: general Power Management feature set, Advanced Power Management feature set, and Power-Up in Standby (PUIS) feature set.

The Advanced Power Management and PUIS features can be enabled or disabled using the SET FEATURES command.

The Power Management feature set includes the following commands:

- CHECK POWER MODE
- IDLE
- IDLE IMMEDIATE
- SLEEP
- STANDBY
- STANDBY IMMEDIATE

5.3 Security Mode Feature Set

The Intel SSD Pro 1500 Series supports the ATA Security Feature Set, which consists of:

- SECURITY SET PASSWORD
- SECURITY UNLOCK
- SECURITY ERASE PREPARE
- SECURITY ERASE UNIT
 - o Normal Mode – Full NAND erase of user available space and spare area
 - o Enhanced Mode – Cryptographically erase data
- SECURITY FREEZE LOCK
- SECURITY DISABLE PASSWORD

Note: Security features disabled and not supported if Opal activated.

5.4 TCG Opal SSC Support

The Intel SSD Pro 1500 Series drive is a Self-Encrypting Drive (SED) that supports the TCG Opal SSC Specification Version 1.0 Rev 3.0 mandatory commands along with the following features:

- PSID (Physical presence Security ID) Revert for SSD Repurposing
- AES mode: XTS-AES 256-bit
- FIPS 197 certified

Once Opal is activated by user (Locking SP in “Manufactured” state), per Opal specification, ATA Security Features are not supported by the drive. Word 82, bit 1 and Word 128, bit 0 will indicate no support (bit = 0). Host should not attempt any ATA Security Features when Opal activated. Table 13 indicates changes in ID Device word values when Opal activated.

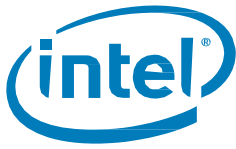
**Table 13: ID Device Changes with Opal Activation**

Word	Description	Value (Opal Deactivated)	Value (Opal Activated)
82	Features/Command Sets Support(Bit 1 Security Support)	746Bh	7469h
89	SECURITY ERASE UNIT Time	0002h	0000h
90	ENHANCED SECURITY ERASE UNIT Time	0001h	0000h
92	Master Password Revision Code	FFFEh	0000h
128	Security Status	0021h	0000h

5.5 SMART Command Set

The Intel SSD Pro 1500 Series supports the SMART command set, which consists of:

- SMART READ DATA
- SMART READ ATTRIBUTE THRESHOLDS
- SMART ENABLE/DISABLE ATTRIBUTE AUTOSAVE
- SMART SAVE ATTRIBUTE VALUES
- SMART EXECUTE OFF-LINE IMMEDIATE
- SMART READ LOG SECTOR
- SMART WRITE LOG SECTOR
- SMART ENABLE OPERATIONS
- SMART DISABLE OPERATIONS
- SMART RETURN STATUS

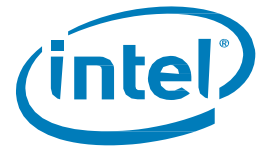


5.5.1 SMART Attributes

Table 14 lists the SMART attributes supported by the Intel SSD Pro 1500 Series; Table 15 lists the corresponding status flags and threshold settings.

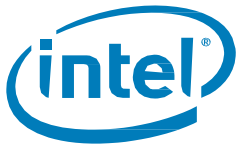
Table 14: SMART Attributes

ID	Attribute	Status Flags						Threshold
		SP	EC	ER	PE	OC	PW	
05h	Re-allocated Sector Count The raw value of this attribute shows the number of retired blocks since leaving the factory (grown defect count).	1	1	0	0	1	0	0 (none)
09h	Power-On Hours Count The raw value reports two values: the first 4 bytes report the cumulative number of power-on hours over the life of the device, the remaining bytes report the number of milliseconds since the last hour increment. The On/Off status of the Device Initiated Power Management (DIPM) feature will affect the number of hours reported. If DIPM is turned On, the recorded value for power-on hours does not include the time that the device is in a "slumber" state. If DIPM is turned Off, the recorded value for power-on hours should match the clock time, as all three device states are counted: active, idle and slumber.	1	1	0	0	1	0	0 (none)
0Ch	Power Cycle Count The raw value of this attribute reports the cumulative number of power cycle events over the life of the device.	1	1	0	0	1	0	0 (none)
AAh	Available Reserved Space	1	1	0	0	1	1	10
ABh	Program Fail Count The raw value of this attribute shows total count of program fails and the normalized value, beginning at 100, shows the percent remaining of allowable program fails.	1	1	0	0	1	0	0 (none)
ACh	Erase Fail Count The raw value of this attribute shows total count of erase fails and the normalized value, beginning at 100, shows the percent remaining of allowable erase fails.	1	1	0	0	1	0	0 (none)
AEh	Unexpected Power Loss The raw value of this attribute reports the cumulative number of unsafe (unclean) shutdown events over the life of the device. An unsafe shutdown occurs whenever the device is powered off without STANDBY IMMEDIATE being the last command	1	1	0	0	1	0	0 (none)



Intel® Solid-State Drive Pro 1500 Series (2.5-inch)

ID	Attribute	Status Flags						Threshold
		SP	EC	ER	PE	OC	PW	
B7h	SATA Downshift Count The count of the number of times SATA interface selected lower signaling rate due to error.	1	1	0	0	1	0	0
B8h	End-to-End Error Detection Count Reports number of errors encountered during end-to-end error detection within the SSD data path.	1	1	0	0	1	1	90
BBh	Uncorrectable Error Count The raw value shows the count of errors that could not be recovered using Error Correction Code (ECC).	1	1	0	0	1	0	0 (none)
BEh	Temperature Reports real-time temperature of drive as measured by temperature sensor on drive PCB. The normalized value reports the current temperature value. The raw value shows current, lifetime highest and lifetime lowest temperatures. Byte 1:0 = current temp Celsius; Byte 3:2 = lifetime highest temp Celsius; Byte 5:4 = lifetime lowest temp Celsius.	1	1	0	0	1	0	0(none)
C0h	Power-Off Retract Count (Unsafe Shutdown Count) The raw value of this attribute reports the cumulative number of unsafe (unclean) shutdown events over the life of the device. An unsafe shutdown occurs whenever the device is powered off without STANDBY IMMEDIATE being the last command.	1	1	0	0	1	0	0 (none)
C7h	CRC Error Count The total number of encountered SATA interface cyclic redundancy check (CRC) errors.	1	1	0	0	1	0	0 (none)
E1h	Host Writes The raw value of this attribute reports the total number of sectors written by the host system. The raw value is increased by 1 for every 65,536 sectors (32MB) written by the host.	1	1	0	0	1	0	0 (none)
E2h	Timed Workload Media Wear Measures the wear seen by the SSD (since reset of the workload timer, attribute E4h), as a percentage of the maximum rated cycles.	1	1	0	0	1	0	0 (none)
E3h	Timed Workload Host Read/Write Ratio Shows the percentage of I/O operations that are read operations (since reset of the workload timer, attribute E4h).	1	1	0	0	1	0	0 (none)
E4h	Timed Workload Timer Measures the elapsed time (number of minutes since starting this workload timer).	1	1	0	0	1	0	0 (none)
E8h	Available Reserved Space This attribute reports the number of reserve blocks remaining. The normalized value begins at 100 (64h), which corresponds to 100 percent	1	1	0	0	1	1	10



ID	Attribute	Status Flags						Threshold
		SP	EC	ER	PE	OC	PW	
	availability of the reserved space. The threshold value for this attribute is 10 percent availability.							
E9h	Media Wearout Indicator This attribute reports the number of cycles the NAND media has undergone. The normalized value declines linearly from 100 to 1 as the average erase cycle count increases from 0 to the maximum rated cycles. Once the normalized value reaches 1, the number will not decrease, although it is likely that significant additional wear can be put on the device.	1	1	0	0	1	0	0 (none)
F1h	Total LBAs Written The raw value of this attribute reports the total number of sectors written by the host system. The raw value is increased by 1 for every 65,536 sectors (32MB) written by the host.	1	1	0	0	1	0	0 (none)
F2h	Total LBAs Read The raw value of this attribute reports the total number of sectors read by the host system. The raw value is increased by 1 for every 65,536 sectors (32MB) read by the host.	1	1	0	0	1	0	0 (none)
F9h	Total NAND Writes Raw value reports the number of writes to NAND in 1 GB increments.	1	1	0	0	1	0	0 (none)

Table 15: SMART Attribute Status Flags

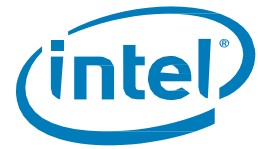
Status Flag	Description	Value = 0	Value = 1
SP	Self-preserving attribute	Not a self-preserving attribute	Self-preserving attribute
EC	Event count attribute	Not an event count attribute	Event count attribute
ER	Error rate attribute	Not an error rate attribute	Error rate attribute
PE	Performance attribute	Not a performance attribute	Performance attribute
OC	Online collection attribute	Collected only during offline activity	Collected during both offline and online activity
PW	Pre-fail warranty attribute	Advisory	Pre-fail

5.5.2 SMART Logs

The Intel SSD Pro 1500 Series implements the following Log Addresses: 00h, 02h, 03h, 06h, and 07h.

The Intel SSD Pro 1500 Series implements host vendor specific logs (addresses 80h-9Fh) as read and write scratchpads, where the default value is zero (0). Intel SSD Pro 1500 Series does not write any specific values to these logs unless directed by the host through the appropriate commands.

The Intel SSD Pro 1500 Series also implements a device vendor specific log at address A9h as a read-only log area with a default value of zero (0).



5.6 Device Statistics

In addition to the SMART attribute structure, statistics pertaining to the operation and health of the Intel SSD Pro 1500 Series can be reported to the host on request through the Device Statistics log as defined in the ATA specification.

The Device Statistics log is a read-only GPL/SMART log located at read log address 0x04 and is accessible using READ LOG EXT, READ LOG DMA EXT or SMART READ LOG commands.

The table below lists the Device Statistics supported by the Intel SSD Pro 1500 Series.

Table 16: Device Statistics Log

Page	Offset	Description	Equivalent SMART attribute if applicable
0x00	-	List of Supported Pages	-
0x01 - General Statistics	0x08	Power Cycle Count	0Ch
	0x10	Power-On Hours	09h
	0x18	Logical Sectors Written	E1h
	0x28	Logical Sectors Read	F2h
0x04 - General Errors Statistics	0x08	Num Reported Uncorrectable Errors	BBh
	0x10	Num Resets Between Command Acceptance and Completion	-
0x06 - Transport Statistics	0x08	Num Hardware Resets	-
	0x10	Num ASR Events	-
	0x18	Num Interface CRC Errors	-
0x07 - Solid State Device Statistics	0x08	Percentage Used Endurance Indicator	E9h This statistic counts up from 0 rather than down from 100, and may go beyond 100 for drives that exceed their expected lifetime.

5.7 SMART Command Transport

With SMART Command Transport (SCT), a host can send commands and data to an SSD and receive status and data from an SSD using standard write/read commands to manipulate two SMART Logs:

- Log Address E0h ("SCT Command/Status") — used to send commands and retrieve status
- Log Address E1h ("SCT Data Transfer") — used to transport data

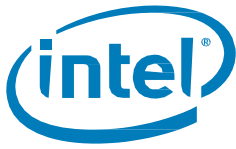
5.8 Data Set Management Command Set

The Intel SSD Pro 1500 Series supports the Data Set Management command set Trim attribute, which consists of:

- DATA SET MANAGEMENT

5.9 Host Protected Area Command Set

- READ NATIVE MAX ADDRESS
- SET MAX ADDRESS
- READ NATIVE MAX ADDRESS EXT
- SET MAX ADDRESS EXT



5.10 48-Bit Address Command Set

The Intel SSD Pro 1500 Series supports the 48-bit Address command set, which consists of:

- FLUSH CACHE EXT
- READ DMA EXT
- READ NATIVE MAX ADDRESS
- READ NATIVE MAX ADDRESS EXT
- READ SECTOR(S) EXT
- READ VERIFY SECTOR(S) EXT
- SET MAX ADDRESS EXT
- WRITE DMA EXT
- WRITE MULTIPLE EXT
- WRITE SECTOR(S) EXT

5.11 General Purpose Log Command Set

The Intel SSD Pro 1500 Series supports the General Purpose Log command set, which consists of:

- READ LOG EXT
- WRITE LOG EXT
- READ LOG DMA EXT
- WRITE LOG DMA EXT

5.12 Native Command Queuing

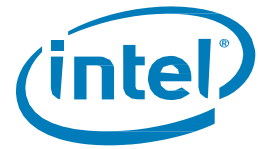
- READ FPDMA QUEUED
- WRITE FPDMA QUEUED

5.13 Software Settings Preservation

The Intel SSD Pro 1500 Series supports the SET FEATURES parameter to enable/disable the preservation of software settings.

5.14 SATA Link Power Management (LPM)

The Intel SSD Pro 1500 Series supports the SET FEATURES parameter to enable Device Initiated Power Management (DIPM). The SSD also supports Host Initiated Power Management (HIPM).



5.15 DevSleep

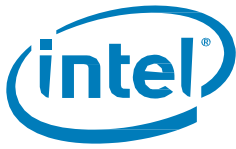
The Intel SSD Pro 1500 Series supports the DevSleep feature. DevSleep must be enabled on the device by the host system through the SET FEATURES command. If DevSleep is enabled by the host, the host must drive the DevSleep signal to proper assert/de-assert voltage levels according to the SATA specification. Entry into DevSleep must be preceded by LPM slumber entry by host and device. The Intel SSD Pro 1500 Series also supports DevSleep_to_ReducedPwrState which allows host to wake the drive using normal LPM COMWAKE out-of-band signaling.

For the Intel® SSD Pro 1500 Series, the total time to DevSleep for system active state is recommended to be 6 sec. The AHCI controller has 4 parameters that need to be defined for proper DevSleep operation between the host and drive. Table 17 defines those values for the Intel® SSD Pro 1500 Series drive.

Table 17: DevSleep Control Parameters

Parameter	Definition	Control	Intel® SSD Pro 1500 Values
DITO	DevSleep Idle Time Out – number of milliseconds prior to host asserting DevSleep	Set by Host	Active (lid-up): 375
DM	DITO Multiplier – set once at boot-up	Set by Host	15
MDAT	Minimum Devsleep Assertion Time – minimum time in milliseconds for host to assert devsleep	Reported by Drive	10
DETO	DevSleep Exit Time Out – max time in milliseconds from when DevSleep is negated to when device ready to detect OOB	Reported by Drive	20

Total time to devsleep entry = DITO * (DM+1)



6 Certifications and Declarations

The table below describes the Device Certifications supported by the Intel SSD Pro 1500 Series.

Table 18: Device Certifications and Declarations

Certification	Description
CE Compliant	European Economic Area (EEA): Compliance with the essential requirements of EC Council Directives Low Voltage Directive (LVD) 2006/95/EC, EMC Directive 2004/108/EC and Directive 2011/65/EU.
UL Certified	Certified Underwriters Laboratories, Inc. Bi-National Component Recognition; UL 60950-1, 2nd Edition, 2007-03-27 (Information Technology Equipment - Safety - Part 1: General Requirements) CSA C22.2 No. 60950-1-07, 2nd Edition, 2007-03 (Information Technology Equipment - Safety - Part 1: General Requirements)
C-Tick Compliant	Compliance with the Australia/New Zealand Standard AS/NZS3548 and Electromagnetic Compatibility (EMC) Framework requirements of the Australian Communication Authority (ACA).
BSMI Compliant	Compliance to the Taiwan EMC standard CNS 13438: Information technology equipment - Radio disturbance Characteristics - limits and methods of measurement, as amended on June 1, 2006, is harmonized with CISPR 22: 2005.04.
KCC	Compliance with paragraph 1 of Article 11 of the Electromagnetic Compatibility Control Regulation and meets the Electromagnetic Compatibility (EMC) Framework requirements of the Radio Research Laboratory (RRL) Ministry of Information and Communication Republic of Korea.
Microsoft WHCK	Microsoft Windows Hardware Certification Kit
RoHS Compliant	Restriction of Hazardous Substance Directive
VCCI	Voluntary Control Council for Interface to cope with disturbance problems caused by personal computers or facsimile.
SATA-IO	Indicates certified logo program from Serial ATA International Organization.
Low Halogen	Applies only to brominated and chlorinated flame retardants (BFRs/CFRs) and PVC in the final product. Intel components as well as purchased components on the finished assembly meet JS-709 requirements, and the PCB/substrate meet IEC 61249-2-21 requirements. The replacement of halogenated flame retardants and/or PVC may not be better for the environment.



7 Appendix

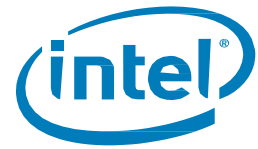
The table below describes the sector data returned from an identify device command

Table 19: Identify Device Returned Sector Data

Word	F = Fixed V = Variable X = Both	Default Value (Opal Ready)	Description
0	F	0040h	General configuration bit-significant information
1	X	3FFFh	Obsolete - Number of logical cylinders (16,383)
2	V	C837h	Specific configuration
3	X	0010h	Obsolete - Number of logical heads (16)
4-5	X	0h	Retired
6	X	003Fh	Obsolete - Number of logical sectors per logical track (63)
7-8	V	0h	Reserved for assignment by the CompactFlash* Association (CFA)
9	X	0h	Retired
10-19	F	varies	Serial number (20 ASCII characters)
20-21	X	0h	Retired
22	X	0h	Obsolete
23-26	F	varies	Firmware revision (8 ASCII characters)
27-46	F	varies	Model number (Intel® Solid-State Drive)
47	F	8010h	7:0—Maximum number of sectors transferred per interrupt on multiple commands
48	F	4000h	Reserved
49	F	2F00h	Capabilities
50	F	4000h (4001h)	Capabilities
51-52	X	0h	Obsolete
53	F	0007h	Words 88 and 70:64 valid
54	X	3FFFh	Obsolete - Number of logical cylinders (16,383)
55	X	0010h	Obsolete - Number of logical heads (16)
56	X	003Fh	Obsolete - Number of logical sectors per logical track (63)
57-58	X	00FBFC10h	Obsolete
59	V	0110h	Number of sectors transferred per interrupt on multiple commands
60-61	F	varies	Total number of user-addressable sectors
62	X	0h	Obsolete
63	F	0007h	Multi-word DMA modes supported/selected
64	F	0003h	PIO modes supported
65	F	0078h	Minimum multiword DMA transfer cycle time per word
66	F	0078h	Manufacturer's recommended multiword DMA transfer cycle time
67	F	0078h	Minimum PIO transfer cycle time without flow control
68	F	0078h	Minimum PIO transfer cycle time with IORDY flow control
69	F	4010h	Additional Supported



Word	F = Fixed V = Variable X = Both	Default Value (Opal Ready)	Description
70	F	0h	Reserved
71-74	F	0h	Reserved for IDENTIFY PACKET DEVICE command
75	F	001Fh	Queue depth
76	F	270Eh	Serial ATA capabilities
77	F	0086h	Reserved for future Serial ATA definition
78	F	054Ch	Serial ATA features supported
79	V	0040h	Serial ATA features enabled
80	F	03FCh	Major version number
81	F	FFFFh	Minor version number
82	F	746Bh	Command set supported
83	F	7429h	Command sets supported
84	F	6163h	Command set/feature supported extension
85	V	7469h	Command set/feature enabled
86	V	B409h	Command set/feature enabled
87	V	6163h	Command set/feature default
88	V	407Fh	Ultra DMA Modes
89	F	0002h	Time required for security erase unit completion
90	F	0001h	Time required for enhanced security erase completion
91	V	00FEh	Current advanced power management value
92	V	FFFEh	Master Password Revision Code
93	F	0h	Hardware reset result: the contents of bits (12:0) of this word shall change only during the execution of a hardware reset
94	V	0h	Vendor's recommended and actual acoustic management value
95	F	0h	Stream minimum request size
96	V	0h	Streaming transfer time - DMA
97	V	0h	Streaming access latency - DMA and PIO
98-99	F	0h	Streaming performance granularity
100-103	V	varies	Maximum user LBA for 48-bit address feature set
104	V	0h	Streaming transfer time - PIO
105	F	0001h	Reserved
106	F	4000h (6003h)	Physical sector size / logical sector size
107	F	0h	Inter-seek delay for ISO-7779 acoustic testing in microseconds
108-111	F	varies	Unique ID
112-115	F	0h	Reserved for world wide name extension to 128 bits
116	V	0h	Reserved for technical report
117-118	F	0h	Words per logical sector
119	F	401Ch	Supported settings



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Word	F = Fixed V = Variable X = Both	Default Value (Opal Ready)	Description
120	F	401Ch	Command set/feature enabled/supported
121-126	F	0h	Reserved
127	F	0h	Removable Media Status Notification feature set support
128	V	0021h	Security status
129-159	X	varies	Vendor-specific
160	F	0h	CompactFlash Association (CFA) power mode 1
161-168	X	0h	Reserved for assignment by the CFA
169	X	0001h	Data set management Trim attribute support
170-173	F	0h	Additional Product Identifier
174-175	F	0h	Reserved
176-205	V	0h	Current media serial number
206	X	0025h	SCT Command Transport
207-208	X	0h	Reserved
209	X	4000h	Alignment of logical blocks within a physical block
210-211	X	0h	Write-Read-Verify Sector Count Mode 3 (DWord)
212-213	X	0h	Write-Read-Verify Sector Count Mode 2 (DWord)
214	X	0h	NV Cache Capabilities
215-216	X	0h	NV Cache Size in Logical Blocks (DWord)
217	X	0001h	Nominal media rotation rate
218	X	0h	Reserved
219	X	0h	NV Cache Options
220	X	0h	Write-Read-Verify feature set
221	X	0h	Reserved
222	X	103Fh	Transport major version number
223	X	0h	Transport minor version number
224-229	X	0h	Reserved
230-233	X	0h	Extended Number of User Addressable Sectors (QWord)
234	X	0002h	Minimum number of 512-byte data blocks per DOWNLOAD MICROCODE command for mode 03h
235	X	0400h	Maximum number of 512-byte data blocks per DOWNLOAD MICROCODE command for mode 03h
236-254	X	0h	Reserved
255	X	varies	Integrity word

Note:

F = Fixed. The content of the word is fixed and does not change. For removable media devices, these values may change when media is removed or changed.

V = Variable. The state of at least one bit in a word is variable and may change depending on the state of the device or the commands executed by the device.

X = F or V. The content of the word may be fixed or variable.